

CNOT-dihedral circuits

Another class of circuits which can be **efficiently represented** and **optimally** (in some cases) synthesized are **CNOT-dihedral circuits**

Def'n (CNOT-dihedral)

CNOT-dihedral circuits of order n are those over $\{X, \text{CNOT}, R_n = R_z(2\pi/n)\}$

Fact

$\{X, R_n\}$ generate the **dihedral group of order n , D_n**

Fact

CNOT and X together generate the **affine** permutations

$$(A, \vec{b}) \in GA(n, \mathbb{Z}_2) \simeq GL(n, \mathbb{Z}_2) \ltimes \mathbb{Z}_2^n$$

$$(A, \vec{b}) \vec{x} = A\vec{x} \oplus \vec{b}$$

$$\text{E.g. } X|x\rangle = |x \oplus 1\rangle = |(I, 1)x\rangle$$

Intuitively then, CNOT-dihedral circuits compute an affine on n basis states, together with a **phase** which is a sum of phases on affine combinations of basis states

$$\text{E.g. } \begin{array}{c} \text{---} \text{---} \\ | \quad | \\ \oplus \quad \square \end{array} \text{ sends } |x\rangle|y\rangle \xrightarrow{\text{CNOT}} |x\rangle|x \oplus y\rangle \xrightarrow{I} \omega^{x \oplus y} |x\rangle|x \oplus y\rangle$$

$$\begin{array}{c} \text{---} \text{---} \\ | \quad | \\ \square \quad \square \end{array} \text{ sends } |x\rangle \xrightarrow{X} |x \oplus 1\rangle \xrightarrow{I} \omega^{x \oplus 1} |x \oplus 1\rangle$$

Def'n

Given $\vec{x}, \vec{y} \in \mathbb{Z}_2^n$, $\chi_{\vec{y}}(\vec{x}) = \vec{x} \cdot \vec{y} = x_1 y_1 \oplus \dots \oplus x_n y_n$

Representation of CNOT-dihedral groups

Prop (phase polynomial representation)

Let C be a circuit over $\{X, \text{CNOT}, R_m\}$. Then

$$[[C]]: |\vec{x}\rangle \mapsto e^{\frac{2\pi i}{m} P(\vec{x})} |A\vec{x} + \vec{b}\rangle$$

Where $A \in GL(n, \mathbb{Z}_2)$, and $P: \mathbb{Z}_2^n \rightarrow \mathbb{Z}_m$ can be written as

$$P(\vec{x}) = \sum_{\vec{y}} a_{\vec{y}} \chi_{\vec{y}}(\vec{x}), \quad a_{\vec{y}} \in \mathbb{Z}_m$$

We call P a **phase polynomial** and $(P, A, \vec{b})$ a **phase polynomial representation** of C

Pf.

By induction on the gates of g . Let

$$[[C]]: |\vec{x}\rangle \mapsto e^{\frac{2\pi i}{m} P(\vec{x})} |A\vec{x} + \vec{b}\rangle$$

$$\text{Then } [[X_i \cdot C]] |\vec{x}\rangle \mapsto e^{\frac{2\pi i}{m} P(\vec{x})} (X_i |A\vec{x} + \vec{b}\rangle)$$

$$\mapsto e^{\frac{2\pi i}{m} P(\vec{x})} |A\vec{x} + (\vec{b} + \vec{e}_i)\rangle$$

$$[[C X_{ij}]] |\vec{x}\rangle \mapsto e^{\frac{2\pi i}{m} P(\vec{x})} (C X_{ij} |A\vec{x} + \vec{b}\rangle)$$

$$\mapsto e^{\frac{2\pi i}{m} P(\vec{x})} |E_{ij} A\vec{x} + E_{ij} \vec{b}\rangle$$

$$[[(R_k)_i \cdot C]] |\vec{x}\rangle \mapsto e^{\frac{2\pi i}{m} P(\vec{x})} ((R_k)_i |A\vec{x} + \vec{b}\rangle)$$

$$\mapsto e^{\frac{2\pi i}{m} P(\vec{x})} e^{2\pi i (A_i \vec{x} \odot \vec{b}_i)} |A\vec{x} + \vec{b}\rangle$$

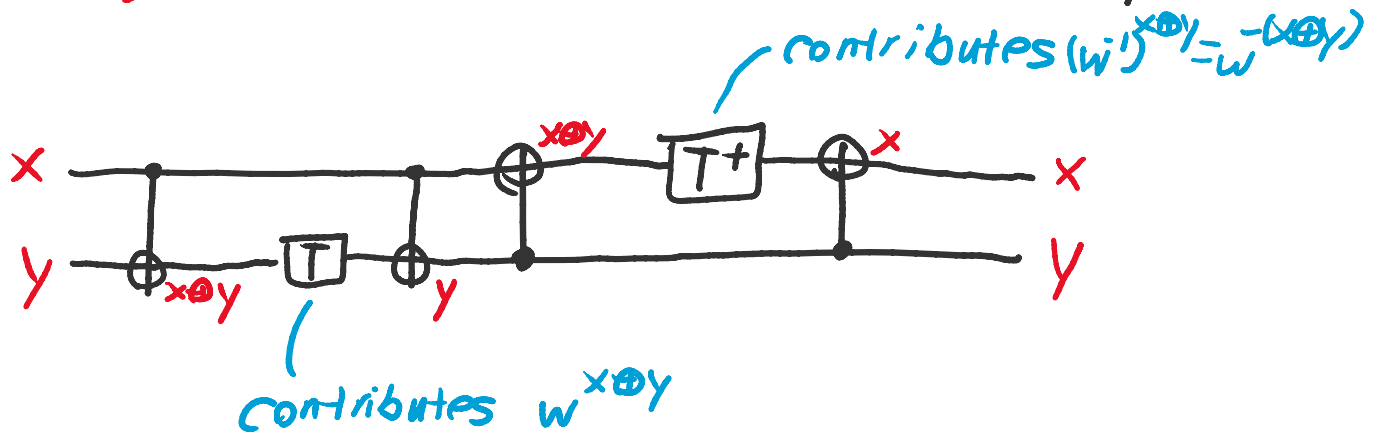
$$\mapsto e^{\frac{2\pi i}{m} (P(\vec{x}) + (-1)^{b_i} \chi_{A_i}(\vec{x}) + b_i)} |A\vec{x} + \vec{b}\rangle$$

Note that $(-1)^{b_i} \chi_{A_i}(\vec{x}) + b_i$ is itself a phase polynomial

Annotated circuits

We can find a Phase polynomial representation by **annotating** the states of qubits (in the comp. basis)

E.g.



Hence,

$$|x\rangle|y\rangle \mapsto w^{x\oplus y} w^{-(x\oplus y)} |x\rangle|y\rangle = |x\rangle|y\rangle$$

Prop.

Given a circuit C over $\{X, \text{CNOT}, R_n\}$, a phase polynomial representation of C can be computed in time and space **linear** in the volume of C ,

$$|C| = (\text{num qubits of } C) \cdot (\text{num gates of } C)$$

We call the bitstrings $\vec{y} \in \mathbb{Z}_2^n$ where $a_y \neq 0$ the **Support** of a phase polynomial P , denoted **supp**(P). While phase polynomials are **NOT** unique, e.g.

$$\sum_{\vec{y}} x_{\vec{y}}(\vec{x}) \equiv 0 \pmod{8} \quad \forall \vec{x}$$

The **canonical** phase polynomial of C , obtained by annotating the circuit and collecting the phases satisfies

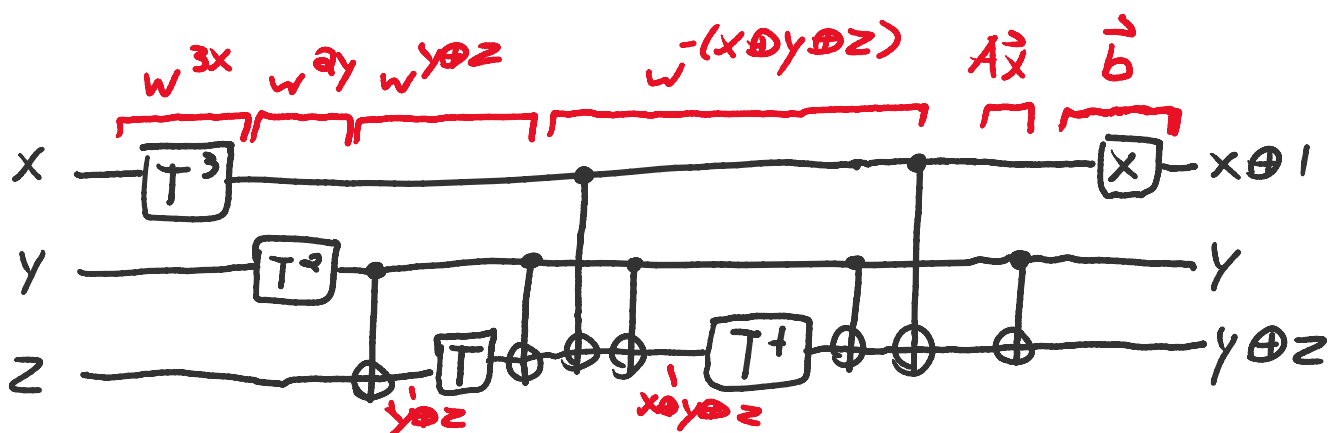
$$|\text{supp}(P)| \leq \# R_n \text{ gates in } C$$

Synthesis of CNOT-dihedral ops

Intuitively, a phase polynomial representation $(P, A, \vec{b})$ corresponds to a sequence of **phase rotations** on **parities** of $\vec{x}$, followed by a final **Affine permutation**, so we can easily synthesize such a circuit.

E.g.
Let $U: |xyz\rangle \mapsto e^{i\pi/8(3x+2y+(y\oplus z)-(x\oplus y\oplus z))} |(x\oplus y, y\oplus z)\rangle$

We can implement U over $\{CNOT, X, T = R_8\}$ as so:



Prop.

Let $P(\vec{x}) = \sum_{\vec{y}} a_{\vec{y}} x_{\vec{y}}(\vec{x})$, $a_{\vec{y}} \in \mathbb{Z}_n$ and $(A, \vec{b}) \in GA(m, \mathbb{Z}_n)$.

Then $U: |\vec{x}\rangle \mapsto e^{2\pi i/n P(\vec{x})} |A\vec{x} + \vec{b}\rangle$ can be synthesized over $\{CNOT, X, R_n\}$ in time polynomial in n and with $|\text{supp}(P)| R_n^k$ gates, $k \in \mathbb{Z}_n$

Note:

If $n = 2^8$, i.e. $R_n = T$, then any **even** power of T is a Clifford gate, and any **odd** power needs only 1 T gate to implement, since $T^{2k+1} = T \cdot T^{2k}$

Hence, **T-count** = $|\text{supp}(P \bmod 2)|$

Synthesizing the Toffoli gate

Recall that $\overline{\text{C}} = \overline{\text{H}} \cdot \overline{\text{H}}$, where $\overline{\text{H}}$ is the CCZ gate defined by

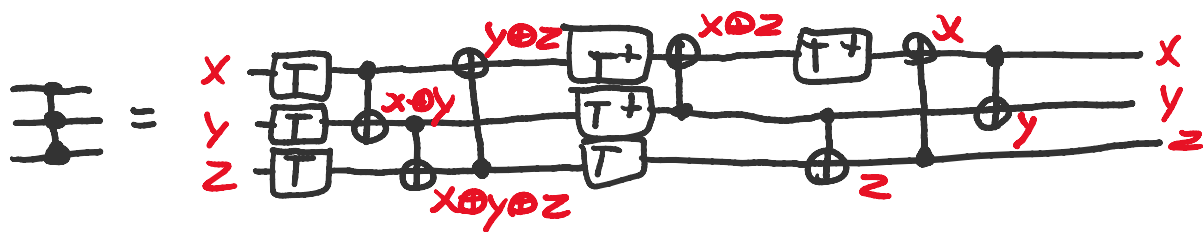
$$CCZ: |xyz\rangle \mapsto (-1)^{xyz} |xyz\rangle$$

Taking the **Fourier expansion*** of $f(x,y,z)=xyz$ gives us

$$f(x, y, z) = \frac{1}{4} [x + y + z - (x \oplus y) - (x \oplus z) - (y \oplus z) + (x \oplus y \oplus z)]$$

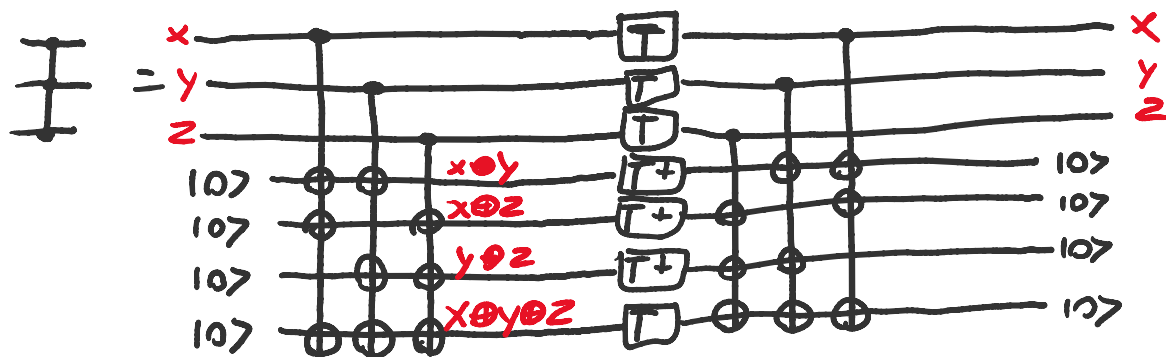
this is a phase polynomial over $\sqrt[4]{-1} = \omega = e^{\pi i/4}$

We can synthesize an explicit circuit for CCZ by preparing each parity and applying either a $w = e^{i\pi/4}$ or $w^{-1} = e^{-i\pi/4}$ rotation (T or $T^\dagger$).



(T-depth | CCZ)

Alternatively, we can synthesize the CCZ (and hence Toffoli gate) in **T-depth 1** by using an ancilla to **"hold"** each parity at once



* Fourier expansion: given $f: \mathbb{Z}_2^n \rightarrow \mathbb{R}$, an expression of f as

$$f(\vec{x}) = \sum_{\vec{y}} \widehat{f}(\vec{y}) \chi_{\vec{y}}(\vec{x})$$

The phase polynomial method

We now have the ingredients for a **phase gate reducing algorithm**, by re-synthesizing CNOT-diagonal circuits. In particular, note

- ① The phase polynomial representation $(P, A, \vec{b})$ of C can be computed efficiently
- ② $|\text{supp}(P)| \leq \# \text{phase gates in } C$
- ③ The phase polynomial representation $(P, A, \vec{b})$ can be synthesized efficiently with $|\text{supp}(P)|$ phase gates

(phase gate, e.g. T gate, optimization for $\{CNOT, X, R_n\}$)

$$C \xrightarrow{\text{annotate}} (P, A, \vec{b}) \xrightarrow{\text{synthesize}} C'$$

$$R_n\text{-count}(C) \leq R_n\text{-count}(C')$$

profit 😊

(Example)

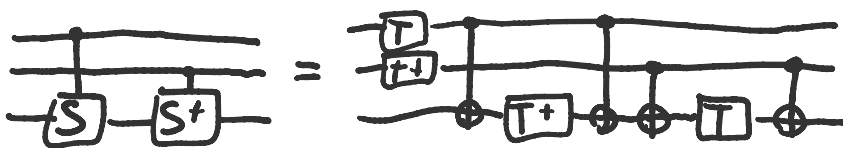
Suppose we want to optimize the circuit:



We start by computing the phase polynomial representation:

$$\begin{aligned} |xyz\rangle &\mapsto w^{x \cdot z - (x \oplus z) \cdot y - z} |xyz\rangle \\ &\mapsto w^{x - (x \oplus z) \cdot y + (y \oplus z)} |xyz\rangle \end{aligned}$$

Re-synthesizing we get a circuit with **$4 < 6$ T gates**



Phase polynomial synthesis problems

More generally, we can ask what is the **best** circuit for $(P, A, \vec{b})$ in some cost metric?

Synthesizing an **optimal** circuit for $(P, A, \vec{b})$ is known as a phase polynomial synthesis problem.

metric	result
R_m -depth	poly-time via Matroid partitioning (t-per)
R_m -count	Equivalent to min-distance decoding for $RM^*(n, n - U_2(n) - 1)$ (hard!)
CNOT-count	NP-hard in restricted cases asymptotically optimal heuristic (Gray-synth)

Note: $U_2(n)$ is the 2-adic valuation of n
(highest power of 2 that divides n)

(Upper & lower bounds on solutions)

metric	lower bound	upper bound
R_m -depth	$O(1)$ (with ancillas)	$O(1)$ (with ancillas)
R_m -count	Covering radius of $RM^*(n, n - U_2(n) - 1)$	$O\left(\frac{n^{U_2(n)-1}}{U_2(n)!}\right) / O(n^2)$ for T-count
CNOT-count	$O(n^2)$ (from gray code)	$O(n^2)$ (from gray code)

CNOT-minimal synthesis

Problem:

Given a phase polynomial representation $(P, A, \vec{b})$, synthesize with minimal CNOT-count

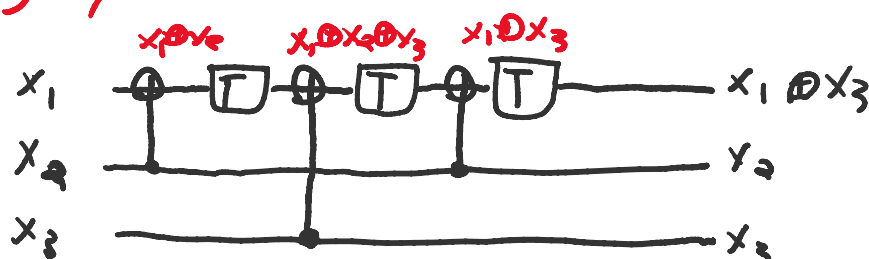
Key idea:

We need to construct a **tour** of $x_2(\vec{x})$ for each $\vec{y} \in \text{supp}\{P\}$ with the minimal CNOT-count

Ex.

$$\text{Let } P(x_1, x_2, x_3) = x_1 \oplus x_2 + x_1 \oplus x_3 + x_1 \oplus x_2 \oplus x_3$$

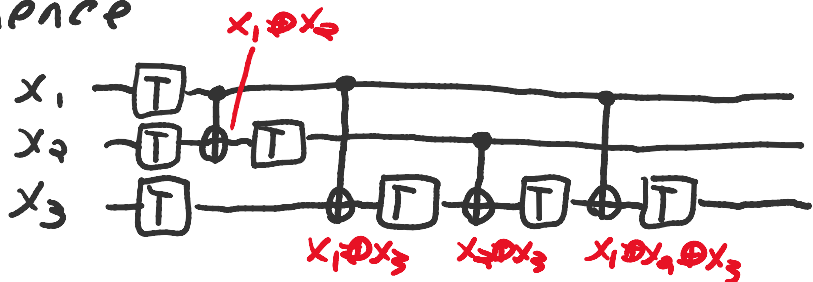
Since $\text{supp}\{P\} \cong \mathbb{Z}_2^2$, we can construct a minimal tour using the **gray code**.



Ex.

$$\text{Let } P(x_1, x_2, x_3) = x_1 + x_2 + x_3 + x_1 \oplus x_2 + x_1 \oplus x_3 + x_2 \oplus x_3 + x_1 \oplus x_2 \oplus x_3$$

Now $\text{supp}\{P\} = \mathbb{Z}_2^3 \setminus \{0\}$. Any tour of $\text{supp}\{P\}$ hence requires at least $2^3 - 1$ CNOT gates. An optimal circuit is hence



Prop

Any set $X \cong \mathbb{Z}_2^k$ can be enumerated **(toured)** optimally with $|X \setminus Y|$ CNOT gates where $Y \subseteq \mathbb{Z}_2^k$ is the set of **inputs**

Gray-synth

Basic idea

Attempt to decompose

$$S = \text{Supp}\{P\} = X_1 \cup X_2 \cup \dots \cup X_n$$

where each $X_i \approx \mathbb{Z}_2^k$ for some k .

we do this by recursively selecting some qubit i which maximizes either the 0 or 1 co-factor,

$$i = \arg\max_i \max_{x \in \{0,1\}} |\{\vec{y} \in S \mid y_i = x\}|$$

Algorithm / queue

1. $C := [], Q := [(S, \{1, \dots, n\}, e)]$
2. while $Q \neq []$
3. $(S, I, i) = Q.\text{pop}$
4. if $S = \emptyset$, then we're done
5. if $S = \{\vec{y}\}$, then
6. for each j s.t. $\vec{y}_j = 1$
7. $C := [\text{CNOT}_{ji}] ++ C$
8. for all remaining $\vec{y}'$, set $\vec{y}' := E_{ij}\vec{y}'$
9. Else
10. $j = \arg\max_{j \in I} \max_{x \in \{0,1\}} |\{\vec{y} \in S \mid y_j = x\}|$
11. $S_0 := \{\vec{y} \in S \mid y_j = 0\}$, $S_1 := \{\vec{y} \in S \mid y_j = 1\}$
12. $Q := [(S_0, I \setminus \{j\}, ivj), (S_1, I \setminus \{j\}, ivj)] ++ Q$
 ($ivj = j$ if $i = e$, i otherwise)

Example

Suppose we wish to Synthesize the phase polynomial

$$f(x) = \frac{1}{8} [x_2 \oplus x_3 + x_1 + x_1 \oplus x_4 + x_1 \oplus x_2 \oplus x_3 + x_1 \oplus x_2 \oplus x_4 + x_1 \oplus x_3]$$

We can visualize Gray-synth as operating on a matrix with columns corresponding to each parity:

$$\begin{array}{cccccc} & x_2 \oplus x_3 & x_1 & x_1 \oplus x_4 & x_1 \oplus x_2 \oplus x_3 & x_1 \oplus x_2 \\ \begin{array}{c} 0 \\ 1 \\ 1 \\ 0 \\ 0 \end{array} & \begin{array}{c} 1 \\ 0 \\ 0 \\ 0 \\ 0 \end{array} & \begin{array}{c} 1 \\ 0 \\ 0 \\ 1 \\ 0 \end{array} & \begin{array}{c} 1 \\ 1 \\ 0 \\ 0 \\ 1 \end{array} & \begin{array}{c} 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{array} & \begin{array}{c} 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{array} \\ & & & & & x_1 \oplus x_2 \oplus x_4 \end{array}$$

We first partition into S_0 & S_1 by the first row

$$S_0 \rightarrow \begin{array}{ccc|ccc} 0 & 1 & 1 & 1 & 1 & 1 \\ 1 & 0 & 0 & 1 & 0 & 0 \\ 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 1 & 0 \end{array} \leftarrow S_1$$

To compute the only parity in S_0 , $x_2 \oplus x_3$, we perform a CNOT between 3 & 2, then update the matrix

$$\begin{array}{c} x_1 \\ x_2 \\ x_3 \\ x_4 \end{array} \begin{array}{c} \text{---} \\ \text{---} \oplus \\ \text{---} \oplus \\ \text{---} \end{array} \begin{array}{c} x_1 \\ x_2 \oplus x_3 \\ x_3 \\ x_4 \end{array} \quad \begin{array}{ccc|ccc} 0 & 1 & 1 & 1 & 1 & 1 \\ 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 0 & 1 & 0 \end{array}$$

We're done with S_0 now, so we pop it off the queue and continue with the remaining, selecting row 4 to construct the next set of co-factors

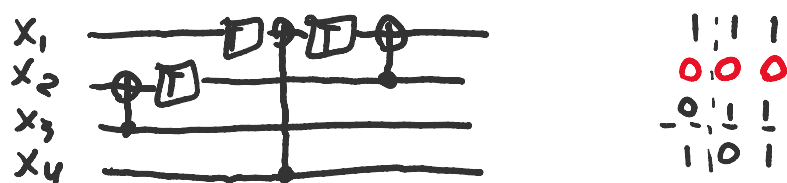
$$S_0 \rightarrow \begin{array}{ccc|ccc} 1 & 1 & 1 & 1 & 1 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 \end{array} \leftarrow S_1$$

The first parity of S_0 is already "computed", so we only need to perform a CNOT between row 4 & row 1:

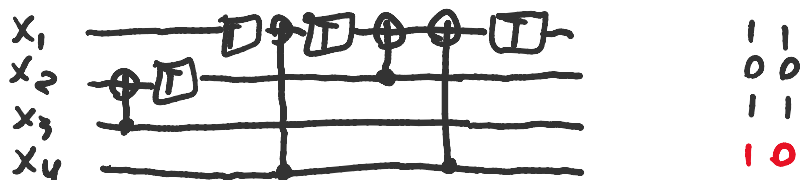
$$\begin{array}{c} x_1 \\ x_2 \\ x_3 \\ x_4 \end{array} \begin{array}{c} \text{---} \\ \text{---} \oplus \\ \text{---} \oplus \\ \text{---} \end{array} \begin{array}{c} x_1 \\ x_2 \\ x_3 \\ x_4 \end{array} \quad \begin{array}{ccc|ccc} 1 & 1 & 1 & 1 & 1 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & 1 & 1 \end{array}$$

Example cont.

Continuing on with S_1 , we zero out the second row with $CNOT_{2,1}$, then partition on row 3



Next we $CNOT_{4,1}$ and move on to S_1



At this point we perform $CNOT_{3,1}$, and then divide into co-factors S_0 & S_1 , which require 0 & 1 $CNOT_n$ resp

